

Enhanced Stability Crystal Oscillator

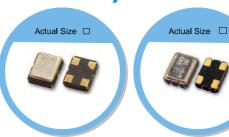
OX-A/OY-A Series-3.2 X 2.5 / 2.5 X 2.0 mm SMD Crystal Oscillator

FEATURE

- Tight Tolerance: ±4 ppm accuracy @25°C, ±4 ppm over -40°C to +85°C
- LVCMOS Output Logic
- Tight symmetry (45 to 55%) available.
- Operation voltage: 1.8V, 2.5V, 3.3V.
- Tri-state enable/disable.
- Femto second phase jitter and-152dBc/Hz at 10kHz offset.

TYPICAL APPLICATION

- Video Distribution

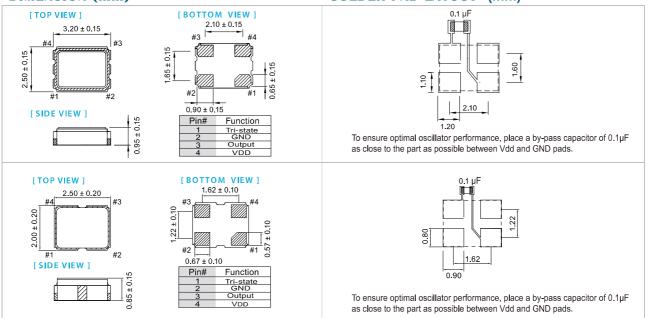


RoHS Compliant

- Wireless Connectivity

DIMENSION (mm)

SOLDER PAD LAYOUT (mm)



FLECTRICAL SPECIFICATION

	Davamatan	3.	3.3V		2.5V		1.8V	
Parameter		Min.	Max.	Min.	Max.	Min.	Max.	Unit
Supply Voltage Variation (VDD) Frequency Range		VDD-10%	V DD+10%	VDD-10%	VDD+10%	VDD-10%	VDD+10%	V
		19	60	19	60	19	60	MHz
Supply Current	19 ≦Fo ≦60 MHz	_	10		7		5	mA
Duty Cycle		45	55	45	55	45	55	%
Output Level (CMOS)	Output High (Logic"1")	2.97		2.25		1.62		V
	Output Low (Logic "0")		0.33		0.25		0.18	
Transition Time: Rise/Fall Time+			8		8		8	nSec
Start Time			5		5		5	mSec
Tri-State(Input to Pin 1) Enable (High voltage or floating) Disable (Low voltage or GND)		2.31		1.75		1.26		V
			0.99		0.75		0.54	
RMS Phase Jitter (integrated 12 kHz ~ 20 MHz)			1		1		1	pSec
Phase Noise @ 26 MHz		-9	-90		-90		-90	
		-1	-115		-115		-115	
		-1	-136		-136		-136	
		-1	-152		-152		-152	
Aging (@25°C 1st yea	r)		±1		±1		±1	ppm
Storage Temp. Range		-55	125	-55	125	-55	125	°C

Standard frequencies are frequencies which the crystal has been designed and does not imply a stock position

FREQ. STABILITY vs. TEMP. RANGE

ppm Temp. (°C)	±5	±10	±15
-10 ~ +60	0	0	0
- 20 ~ +70	\triangle	0	0
- 40 ~ +85	X	0	0

*Inclusive of calibration @ 25°C, operating temperature range, input voltage variation, load variation, aging (1st year), shock, and vibration

Note: not all combination of options are available. Other specifications may be available upon request.

⁺Transition times are measured between 10% and 90% of VDD, with an output load of 15of