RPT7050A

The RPT7050A encompasses Rakon's new patented Pluto+ ASIC. Pluto+ advances on the world famous, 'best-in-class' Pluto ASIC technology by delivering exceptional phase noise and jitter performance and enhanced frequency versus temperature stability. The single chip oscillator with its analogue compensation circuit is capable of sub 0.1ppm frequency stability over an extended temperature range and RMS phase jitter down to 0.13ps for IEEE1588 and SyncE applications. Its patented, unique tilt control ensures lifetime specification compliance, unlike other TCXOs available.

Features

- Best in class frequency versus temperature
- RMS phase jitter down to 0.13ps
- Phase noise <-160dBc/Hz noise floor
- Voltage control and T-sense options available

Applications

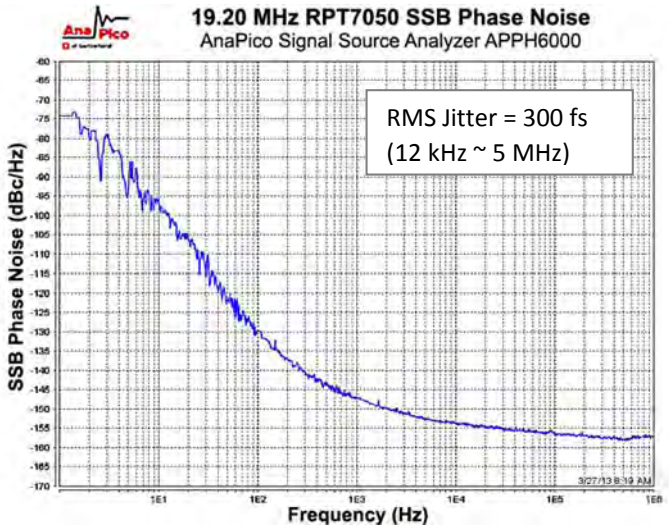
- **Time and frequency reference**
  - Positioning
  - Test and Measurement
  - Telecommunications
  - Hi-Rel / Defense

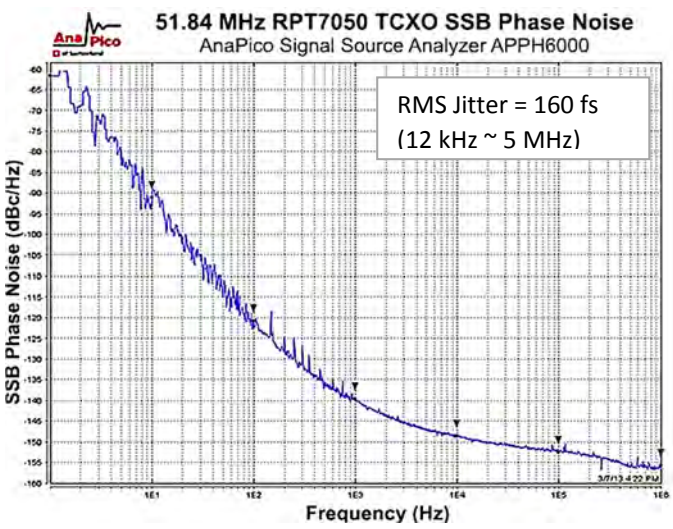
7.0 x 5.0 x 2.0 mm

Standard Specifications

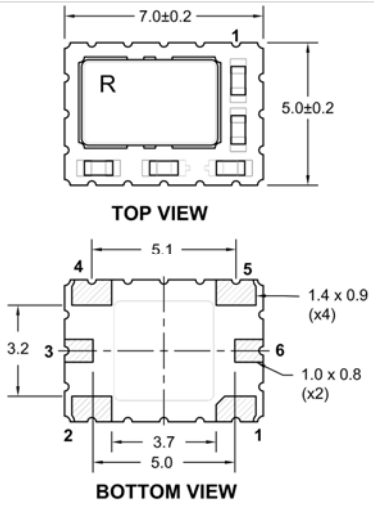
Parameter	Min.	Typ.	Max.	Unit	Test Condition / Description
Nominal frequency		1.25 – 52		MHz	Standard frequencies: 10.0, 12.8, 16.384, 19.2, 19.44, 20.0, 25.0, 26.0, 30.72, 38.88 and 40MHz
Frequency calibration			±1	ppm	Initial accuracy at 25 ± 1°C
Reflow shift			±0.5	ppm	Pre to post reflow ΔF (measured ≥ 60 minutes after reflow)
Operating temperature range	-55		105	°C	
Frequency stability over temperature			±0.05 – ±2.5	ppm	Reference to (Fmax + Fmin)/2. The best available stability depends on the nominal frequency and selected operating temperature range
Supply voltage stability		±0.025		ppm	±5% variation Reference to frequency at nominal V_{CC}
Load sensitivity		±0.05		ppm	• HCMOS, ACMOS: ±5pF variation, • Clipped sine wave / Sine wave: ±10% variation reference to frequency at nominal load
Long term stability (aging) ≤26MHz >26MHz			 ±1 ±2	 ppm/year ppm/year	 ±3ppm/10 years ±5ppm/10 years
Acceleration stability		<2		ppb/*g*	Gamma vector, 3 axes, 30 – 1500Hz
Start-up time			5 – 15	ms	90% amplitude
Supply voltage, V_{CC} Current (C/Sine) Current (Sine) Current (HCMOS) Current (ACMOS)	2.5	 2 8 4 8	5.7	V mA mA mA mA	±5%, standard values are 3.0, 3.3 and 5.0V
Control voltage, Vc	0.5		2.5	V	
Frequency tuning ≤26MHz >26MHz	 ±5 ±7			 ppm ppm	
Root Allan Variance (20MHz)		5		10^{-11}	tau = 1.0s
Oscillator output options					Clipped sine wave, sine wave, HCMOS (LVCMOS & LVTTL compatible as per JESD8C) and ACMOS
Tri-state control Input level low (pin 6) Input level high (pin 6)	 0.6V_{CC}		 0.2V_{CC}	 V V	 Device disabled, output in high impedance state Device enabled and operating

SSB Phase Noise (Typical value at 25°C)



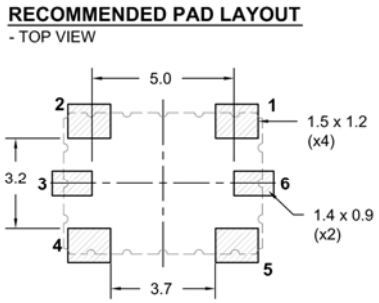


Model Outline and Recommended Pad Layout



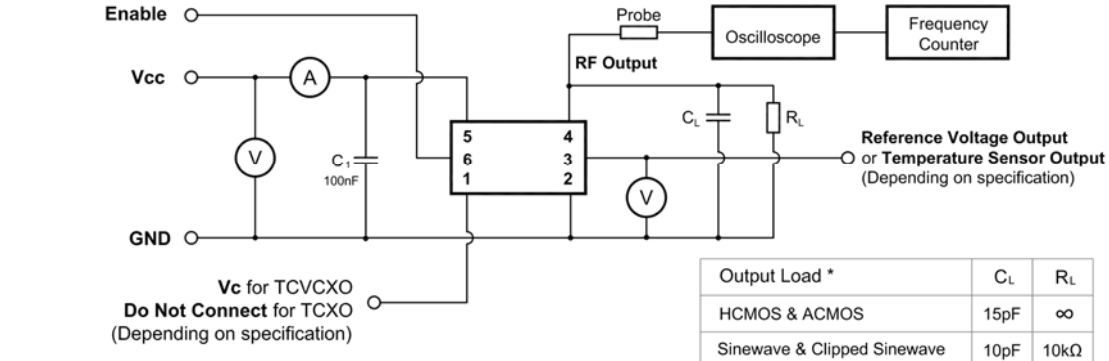
PIN CONNECTIONS	
1 *	Do Not Connect / Vc
2	GND
3 *	Do Not Connect / Vref / Ttemp
4	RF Output
5	Vcc
6	Enable

* Depending on specification



NOTE: Outline unit is mm.

Test Circuit



Output Load *	C_L	R_L
HCMOS & ACMOS	15pF	∞
Sinewave & Clipped Sinewave	10pF	10kΩ
* Inclusive of probe impedance		