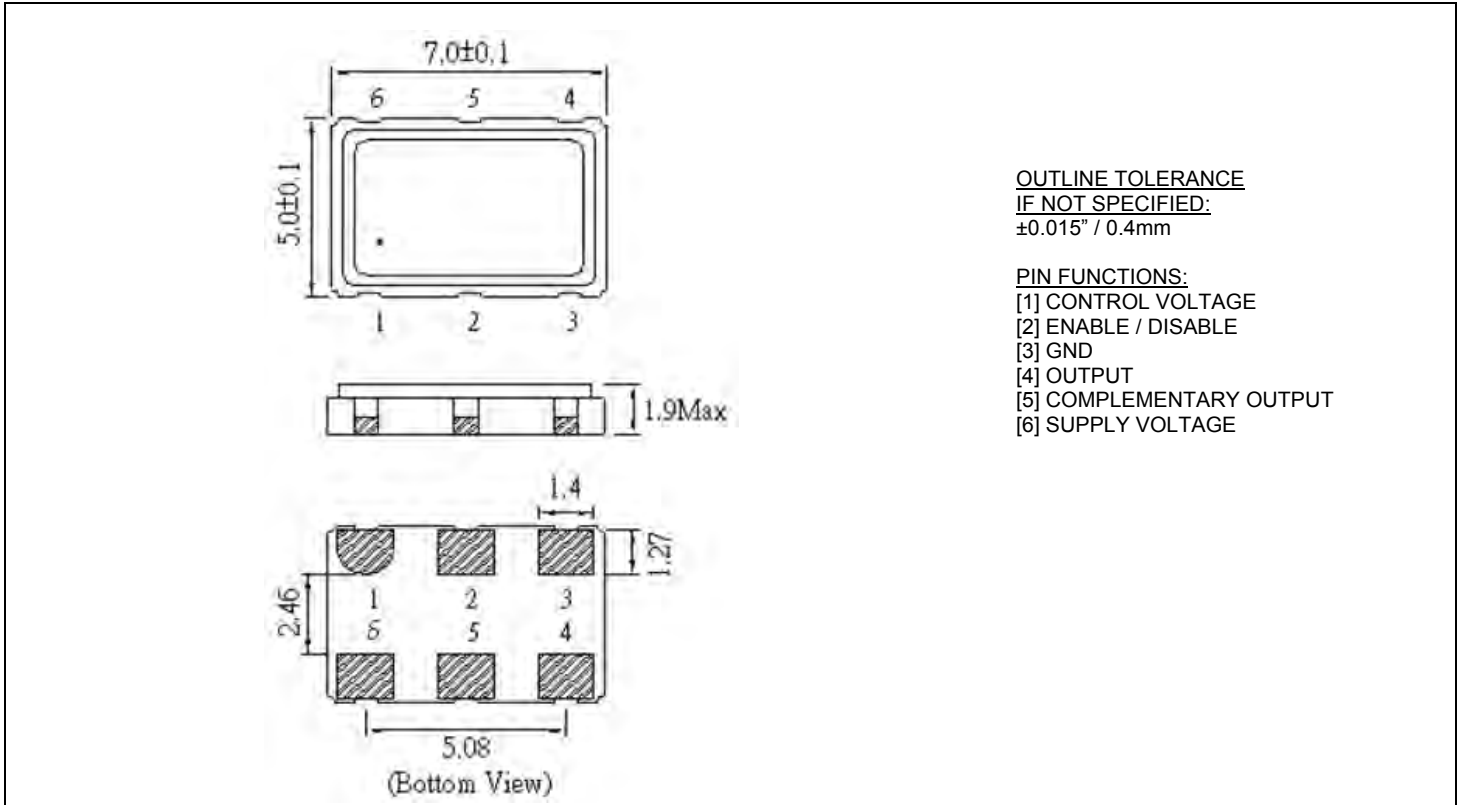




■ ELECTRICAL SPECIFICATION

PARAMETER		VALUE
Frequency Range (F_0)		100.000 ~ 700.000 MHz
Supply Voltage (V_S)		3.3 \pm 10% VDC
Oscillation Mode		PLL
Control Voltage Range (V_{CC})		1.65 \pm 1.65 VDC
Input Current		99 mA max
Frequency Stability		\pm 20 ppm, \pm 25 ppm, \pm 50 ppm, \pm 100 ppm
Frequency Adjustment Range		\pm 50 ppm min, \pm 100 ppm min
Operating Temperature Range		-10 ~ +70°C -40 ~ +85°C
Storage Temperature Range		-55 ~ +125°C
Output LVPECL	Symmetry at 50% V_S	40% ~ 60% Standard 45% ~ 55% Tight
	Rise / Fall Time	1.0 ns max
	Logic "0" Level	1.68 V max
	Logic "1" Level	2.475 V min
	Load (Terminus to V_S-2V)	50 Ω
Enable / Disable Function		Pin 1: High or Open / Output enabled (Pins 4 & 5) Pin 1: Low / Output disabled (High impedance)
RMS Phase Jitter (12kHz ~ 20 MHz)		1 ps max

MECHANICAL SPECIFICATION



PART NUMBERING SYSTEM

TYPE	SERIES	VOLTAGE (V)	STABILITY (ppm)	TEMPERATURE RANGE (°C)	PULLABILITY (ppm)	SYMMETRY (%)	FREQUENCY (MHz)
VPL	7	3: 3.3	20: ± 20 25: ± 25 50: ± 50 10: ± 100	JZ: -10 ~ +70 HZ: -20 ~ +70 D3: -40 ~ +85	50: ± 50 100: ± 100	blank: 40~ 60 T: 45~55	100.000 ~ 700.000

EXAMPLE: VPL7325-D3-100-T-155.520

Surface Mount VP7L Series, LVPECL PLL VCXO, 7.0 x 5.0 mm, 3.3 VDC Supply Voltage, ± 25 ppm Stability from -40°C to $+85^\circ\text{C}$, ± 100 ppm Frequency Adjustment Range, Symmetry 45% to 55%, 155.520 MHz