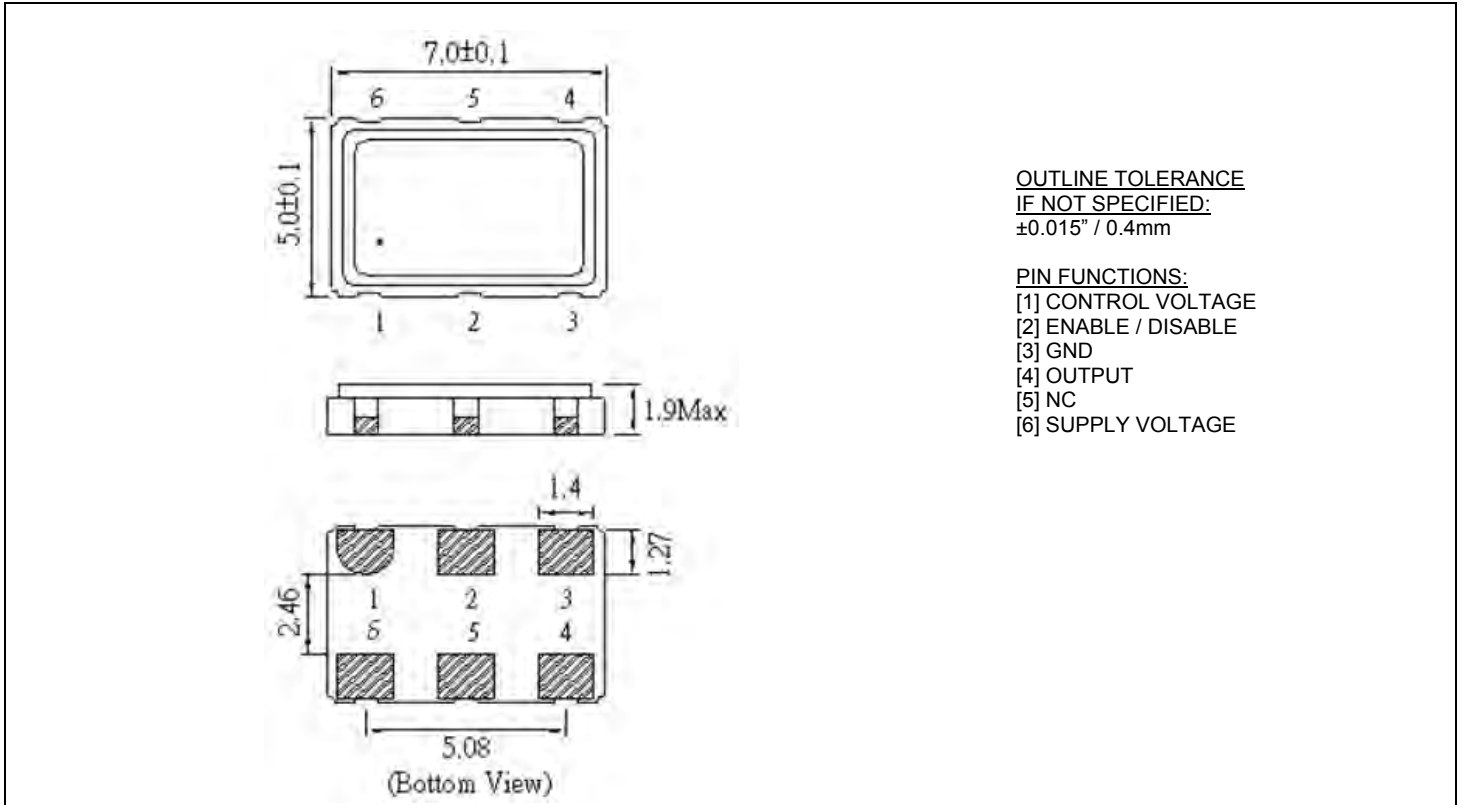




■ ELECTRICAL SPECIFICATION

PARAMETER		VALUE	
Frequency Range (F_o)		1.000 ~ 200.000 MHz	1.000 ~ 40.000 MHz
Supply Voltage (V_s)		3.3 ± 10% VDC	5.0 ± 10% VDC
Control Voltage Range (V_{cc})		1.65 ± 1.35 VDC	2.5 ± 2.0 VDC
Input Current		15 mA max ($F_o \leq 40.00$ MHz)	30 mA max
		50 mA max ($F_o > 40.00$ MHz)	
Frequency Stability		±20 ppm, ±50 ppm, ±100ppm	
Frequency Adjustment Range		±50 ppm min, ±100 ppm min	
Operating Temperature Range		-10 ~ +70°C -40 ~ +85°C	
Storage Temperature Range		-55 ~ +125°C	
Output CMOS	Symmetry at 50% V_s	40% ~ 60% Standard 45% ~ 55% Tight	
	Rise / Fall Time	6 ns max	
	Logic "0" Level	$V_s \times 0.1$ V max	
	Logic "1" Level	$V_s \times 0.9$ V min	
	Load	15 pF max	
Enable / Disable Function		Pin 1: High or Open / Output enabled (Pins 4 & 5) Pin 1: Low / Output disabled (High impedance)	
RMS Phase Jitter (12kHz ~ 20 MHz)		1 ps max	
Peak to Peak Period Jitter		30 ps typ	

MECHANICAL SPECIFICATION



PART NUMBERING SYSTEM

TYPE	SERIES	VOLTAGE (V)	STABILITY (ppm)	TEMPERATURE RANGE (°C)	PULLABILITY (ppm)	SYMMETRY (%)	FREQUENCY (MHz)
VO	7	3: 3.3 5: 5.0	20: ±20 50: ±50 10: ±100	JZ: -10 ~+70 HZ: -20 ~+70 D3: -40 ~+85	50: ±50 100: ±100	blank: 40~ 60 T: 45~55	1.000 ~ 200.000

EXAMPLE: VO7325-D3-100-T-155.520

Surface Mount VO7 Series CMOS VCXO, 7.0 x 5.0 mm, 3.3 VDC Supply Voltage, ±25 ppm Stability from -40°C to +85°C, ±100 ppm Frequency Adjustment Range, Symmetry 45% to 55%, 155.520 MHz