

**Series T7250-T7258, T9250-T9258,
T7001-T7013, T9301-T9313**
Extended Temperature/COTS
XO, 3.3V



Features

- 20kHz to 100MHz frequency range
- 5X7 SMD form factor
- Hermetically sealed for rugged environmental conditions
- Extremely wide operating temperature range accommodates harsh environments
- Crystals are processed with tight angle control to assure best frequency-temperature characteristics
- Units are vacuum baked before sealing at 175°C for 16 hours to eliminate moisture traces and pre-age units for superior stability
- Tristate feature optional
- Equivalent 5V parts are available in [T1250 series](#)
- Solder coating of outer pads upon request



RoHS Status

RoHS
6/6

Tinned pad parts are
5/6 RoHS compliant

Description

Owing to their small size, light weight, and rugged characteristics, these 3.3V HCMOS extended temperature/COTS oscillators fulfill tasks not previously feasible. They are used in applications that take advantage of their extended temperature range and high performance. Twenty four different models (with and without tristate) cover -55°C to +200°C operation and provide frequency selection from 20kHz to 100MHz. They combine excellent long-term reliability, loading characteristics, and superior startup performance.

Applications

- Applications that require an HCMOS 3.3V clock and might be exposed to extremely harsh environmental conditions.

Electrical Specifications

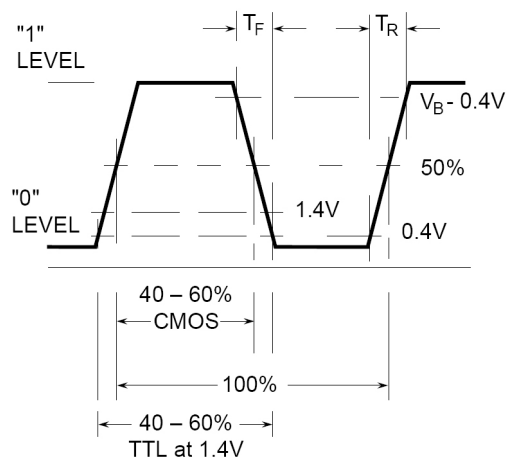
Parameter	Symbol	Condition	Min	Typ	Max	Unit	Note	
Frequency Range	F		0.02		100	MHz		
Frequency Stability	$\Delta F/F$	Overall condition including calibration, temperature voltage and load variation	± 25		± 500	ppm	See Chart	
Operating Temperature	T		-55°		+200°	°C	See Chart	
Aging		First Year After First Year		3 1		ppm ppm/yr	85C	
Supply Voltage	V _{CC}		3.0	3.3	3.6	V		
Supply Current					20	mA		
Output		All units, full range Loads 3 TTL loads, or 10 LSTTL loads, or 15pF CMOS						
Symmetry		TTL and LSTTL @ 1.4V CMOS, @ 50% V _{DD}		40/60 40/60		%		
Rise and Fall Times		TTL and LSTTL from 0.4 to 2.4V CMOS, 15 pF, from 0.4 to (V _{DD} - 0.4) V CMOS, 30 pF, from 0.4 to (V _{DD} - 0.4) V			8 8 10	ns		
Input requirement for pin.1		Output enable - Output disable (Tristate)	pin 1 may float or 2.8V min pin 1 requires 0.4V max					

Environmental and Mechanical Conditions

Parameter	Condition
Shock	1000 Gs, 0.35 ms, ½ sine wave, 3 shocks in each plane
Vibration	10-2000 Hz of 0.06" d.a. or 20 Gs, whichever is less
Humidity	Resistant to 85° R.H. at 85°C
Gross Leak	Each unit checked in 125°C fluorocarbon
Fine Leak	Mass spectrometer leak rate less than 2×10^{-8} atm, cc/sec of helium
Case	Ceramic with glass hermetic seal
Pads*	40 microinch of gold over nickel or tinned (solder coated)
Marking	Epoxy ink or laser engraved
Resistance to Solvents	MIL STD 202, Method 215

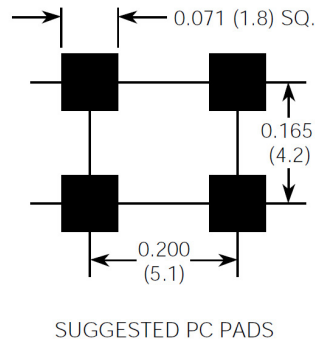
*Tinned by Hot dip solder in accordance with MIL-M38510 (63/37 Sn/Pb)

FIXED OUTPUT	TRISTATE	Frequency Stability	Operating Temperature
Model	Model		
T7250	T9250	±75ppm	-40° to +85°C
T7254	T9254	±100 ppm	0° to +175°C
T7256	T9256	±75 ppm	-55° to +85°C
T7258	T9258	±100 ppm	-40° to +85°C
T7001	T9301	±500 ppm	-55° to +200°C
T7002	T9302	±500 ppm	0° to 200°C
T7003	T9303	±250 ppm	-55° to +200°C
T7004	T9304	±250 ppm	0° to +200°C
T7005	T9305	±250 ppm	-55° to +175°C
T7006	T9306	±250 ppm	0° to +175°C
T7007	T9307	±150 ppm	-55° to +175°C
T7008	T9308	±150 ppm	0° to +175°C
T7009	T9309	±100 ppm	-55° to +125°C
T7010	T9310	±50 ppm	-55° to +85°C
T7011	T9311	±25 ppm	-55° to +85°C
T7012	T9312	±75 ppm	-55° to +125°C
T7013	T9313	±50 ppm	-55° to +125°C

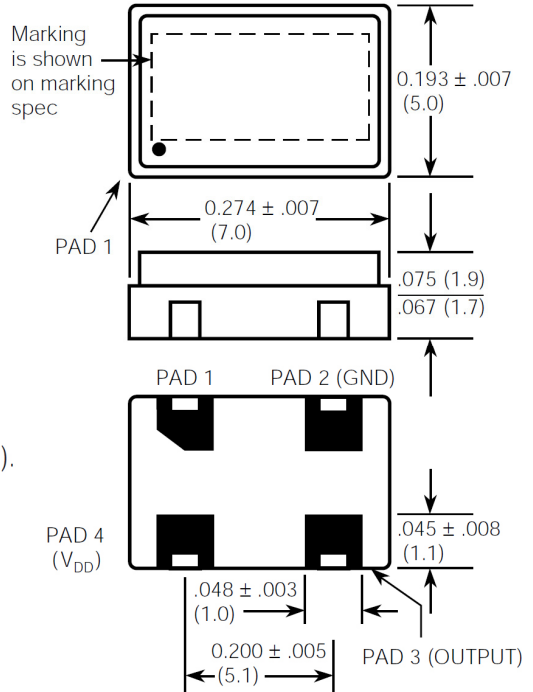


Waveforms

Package Outline



Millimeters are shown in ().



Pin Assignments

Pin	Non-Tristate Models	Tristate Models
1	NOT USED	Floating or 1 : Oscillator runs Ground or 0 : Disable or Tristate
2	Ground and Case	
3	Output	
4	+3.3V, V _{DD}	

How to Order



"T" is SMD (gold pads) T package

"7250" is model type

" " is none
"TL" is tinned pads*
*5/6 RoHs compliant

Marking Specification

